



Veer Madho Singh Bhandari Uttarakhand Technical University Campus Institute Government Women Institute of Technology

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Notice: Smart India Hackathon (SIH) 2026 Internal Hackathon

All students are hereby informed that the institute is organizing the Internal Hackathon for SIH 2026. As per the official Smart India Hackathon guidelines from the Ministry of Education's Innovation Cell (MIC) and AICTE, qualifying through the internal college round is mandatory for nominations to the national stage. Top-performing teams from this internal evaluation will be officially nominated on the SIH portal to represent our institute.

Eligibility & Team Formation Rules

- **Team Size:** Exactly 6 members per team.
- **Interdisciplinary Teams:** Teams can be formed across different branches and academic years within the institute.
- **Team Leader:** Nominate one member as the primary point of contact for official portal registration and updates.

Submission & Evaluation Requirements

- **Problem Statement Selection:** Official SIH problem statements have been announced (<https://sih.gov.in/sih2026PS>)
- **PPT Format:** Standard SIH format (maximum 5-7 slides) covering Problem Statement ID & Title, Proposed Solution & Working Architecture, Tech Stack & Novelty/Uniqueness, and Feasibility, Implementation Plan & Potential Impact (Download link: <https://sih.gov.in/letters/2026/SIH2026-IDEA-Presentation-Format.pptx>).
- **Prototype:** Working demo, UI/UX workflow, system architecture diagram, or simulation models.

Key Dates & Deadlines:

Event / Milestone	Date
Registration & PPT Submission Opens	22th August, 2026
Submission Deadline (Google Form)	12th September, 2026
Internal Hackathon (Pitch & Demo Day)	15 th September, 2026
Final Nominated Teams Announcement	16 th September, 2026

Registration Link: <https://forms.gle/FLGM42NxN2AKmQWr7>

For any technical queries or assistance regarding problem statement mapping, reach out via the official department coordination group or contact the Student Hackathon Coordinator.

(Neetu Prajapati)
SPOC, SIH
Head of Department, CSE